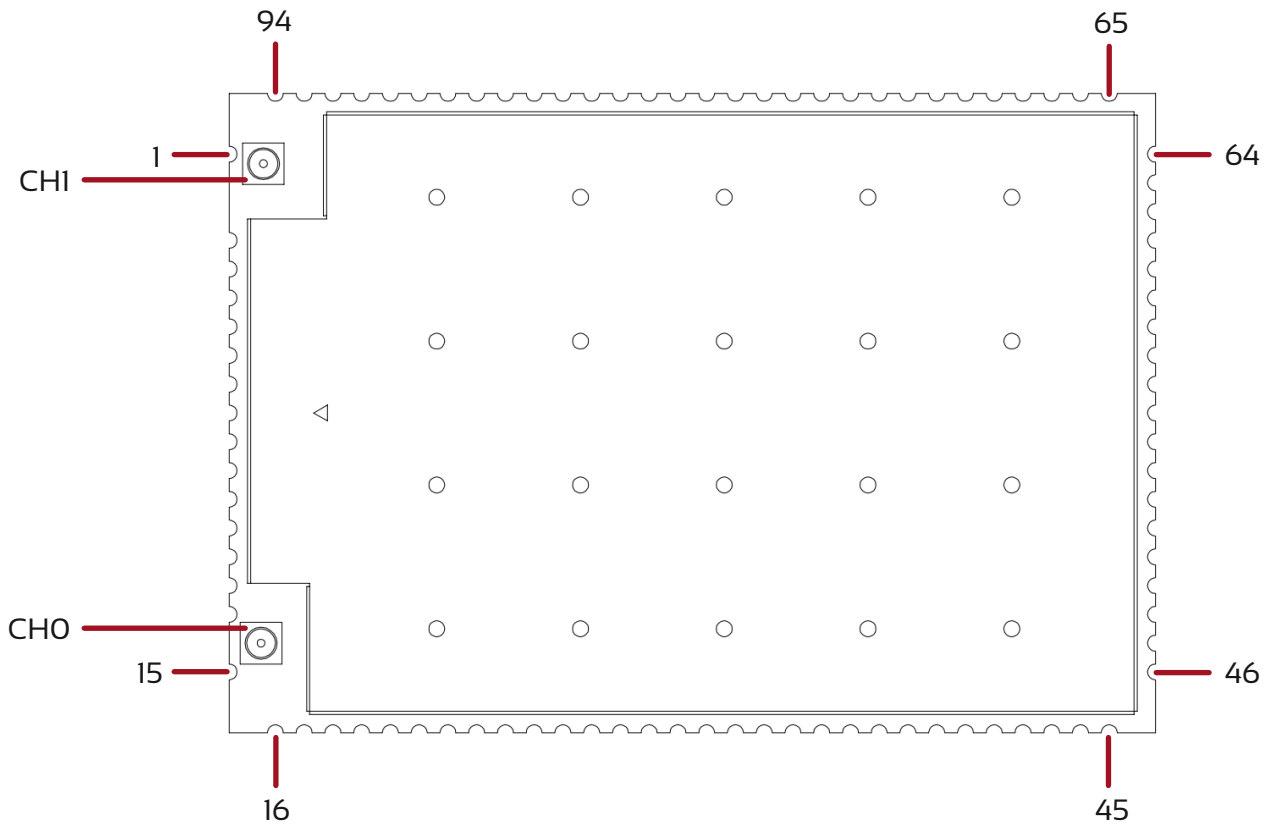


Module Pinout Table

Pin	Signal	Pin	Signal
1	GND	16	GND
2	GND	17	JTAG_RST_N
3	PWRDN_WIFI	18	JTAG_TDO
4	PCIE_WAKE_UP	19	JTAG_TDI
5	PA-5V_5G_1	20	JTAG_TCK
6	GND	21	CHIP_PWD_L
7	PA-5V_2G_1	22	TAG_TRST_N
8	3.3V	23	JTAG_TMS
9	3.3V	24	GND
10	GND	25	UART1_RTS
11	PA-5V_2G_0	26	UART1_TXD
12	GND	27	UART1_RXD
13	PA-5V_5G_0	28	UART1_CTS
14	GND	29	GND
15	GND	30	3.3V
		31	3.3V
		32	3.3V
		33	SS_RST_N
		34	UART0_TXD
		35	UART0_RXD
		36	3.3V
		37	3.3V
		38	GND
		39	12V
		40	12V
		41	SDIO_CMD
		42	SDIO_DAT_1
		43	SDIO_CD
		44	SDIO_DAT_0
		45	SDIO_DAT_3

Pin	Signal	Pin	Signal
46	SDIO_CLK	65	GND
47	SDIO_DAT_2	66	P4_TRX1+
48	P3_1000_LED	67	P4_TRX1-
49	P4_1000_LED	68	P4_TRX2+
50	GND	69	P4_TRX2-
51	P3_TRX3-	70	P4_TRX3+
52	P3_TRX3+	71	P4_TRX3-
53	P3_TRX2-	72	GND
54	P3_TRX2+	73	PCIE_TX-
55	P3_TRX1-	74	PCIE_TX+
56	P3_TRX1+	75	PCIE_RX+
57	P3_TRX0+	76	PCIE_RX-
58	P3_TRX0-	77	PCIE_CLK+
59	GND	78	PCIE_CLK-
60	3.3V	79	GND
61	12V	80	USB_DP
62	GND	81	USB_DM
63	P4_TRX0+	82	GND
64	P4_TRX0-	83	USB3_SS_TX_P
		84	USB3_SS_TX_N
		85	GND
		86	USB3_SS_RX_P
		87	USB3_SS_RX_N
		88	GND
		89	USB3_HS_DP
		90	USB3_HS_DM
		91	GND
		92	PCIE_CLK_REQ_N
		93	PCIE_RST
		94	PCIE_W_DISABLE_L

Module Pinout Diagram



Reflow Temperature Profile

Please refer to IPC/JEDEC standard.
Peak Temperature < 250°C.
Number of Times ≤ 2 times.

